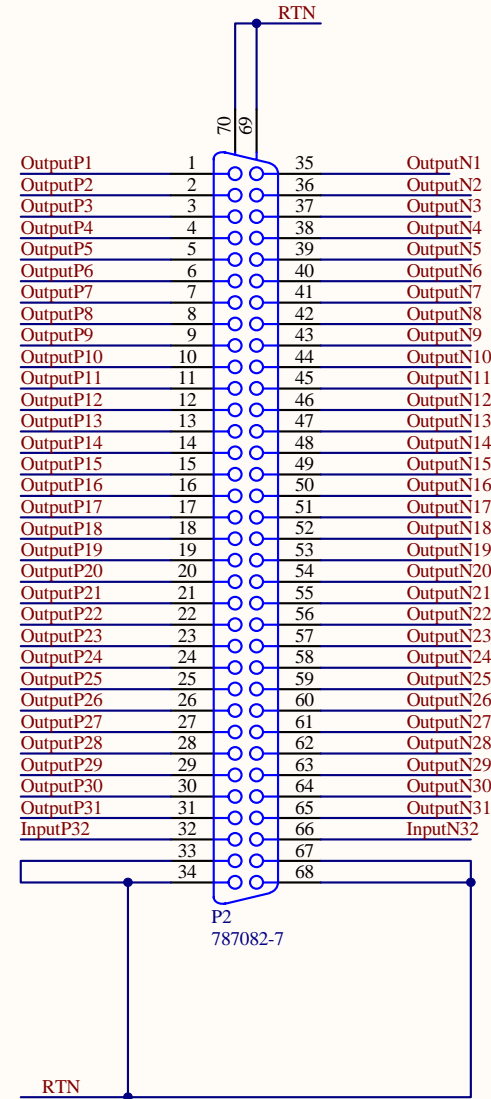
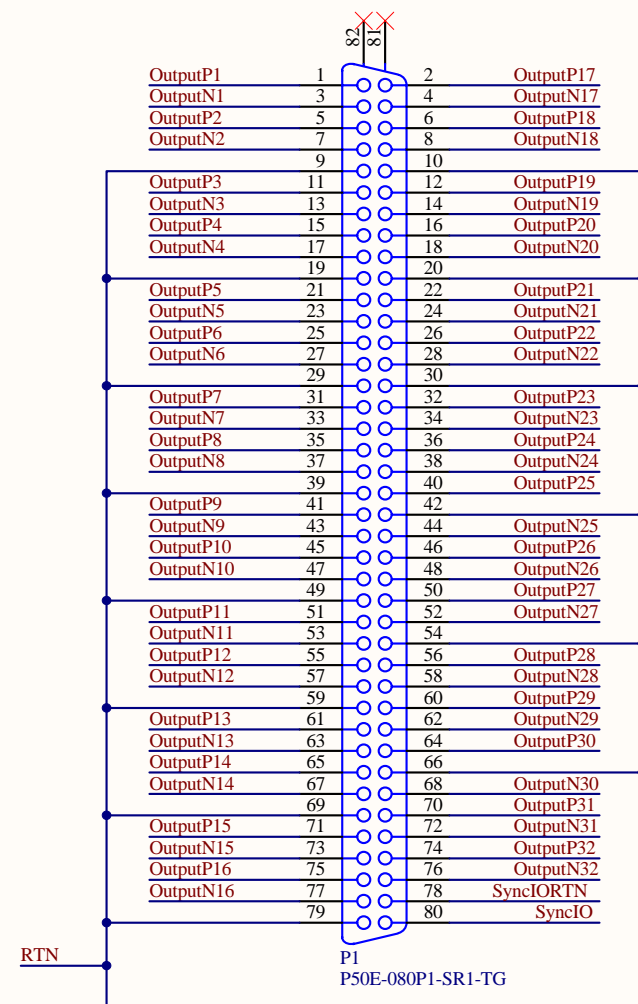


### Diff Inputs from Anti-Alias

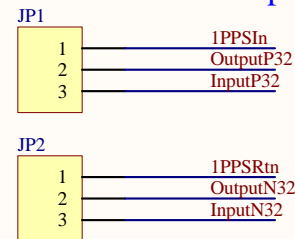


### Diff Outputs to ADC

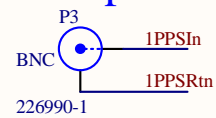


### CH32 Source Selection Jumpers

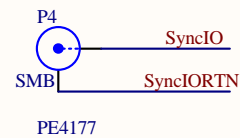
Pos. 1-2= 1PPS from Adapter, 2-3= CH32 from Interface



### Alt Input to CH 32 for 1PPS



### ADC Clock Input



Title <b>PCIX ADC Adapter Board</b>			LIGO Project California Institute of Technology Massachusetts Institute of Technology		LIGO
Size: B	DCC Number: D060060	SCH / PCB Revision: 00	Engineer: J. Heefner	Date: 3/13/2006	Time: 9:14:28 AM
File: C:\Documents and Settings\jay\Desktop\PCIX_ADC_DAC_Adapters\ADC_Adapter.SchDoc					Sheet 1 of 1